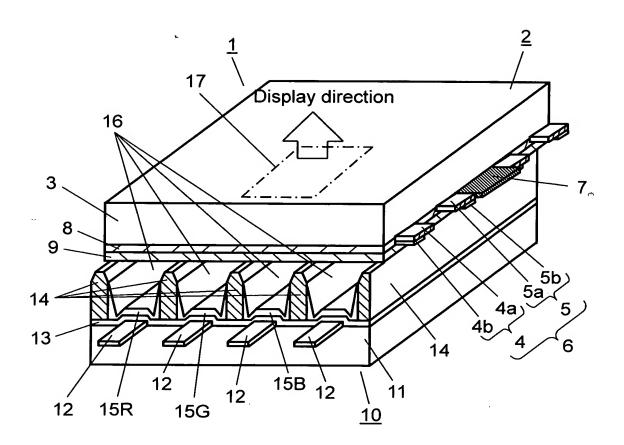
1/5 FIG. 1



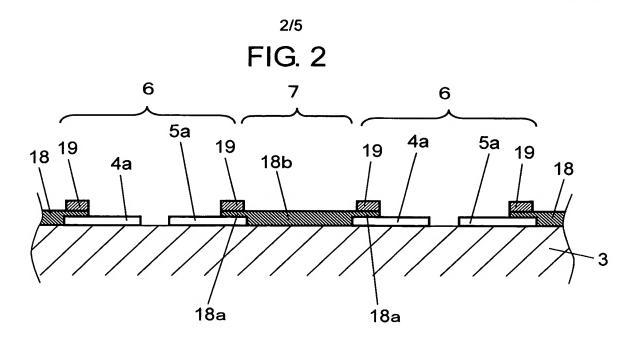
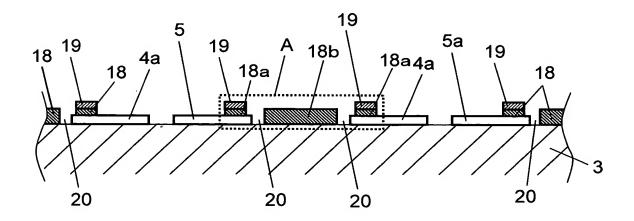
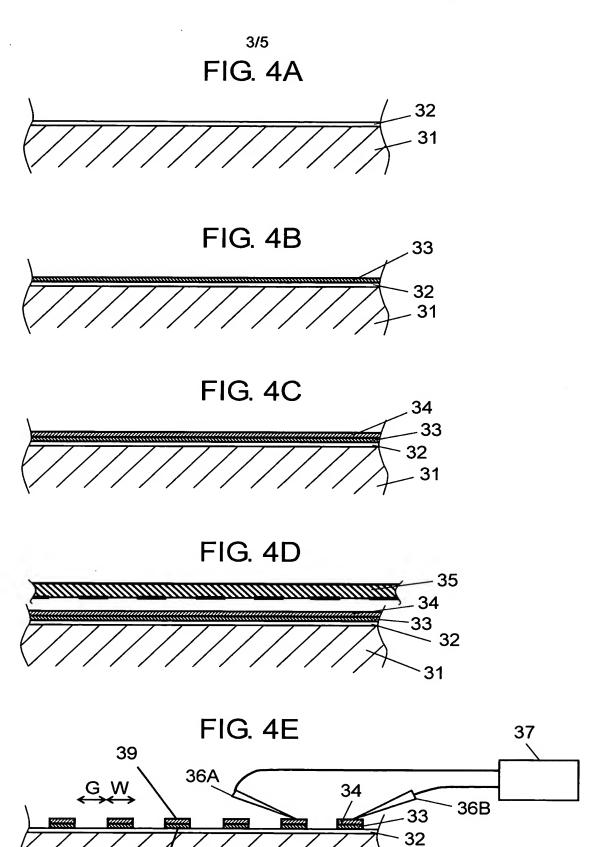


FIG. 3





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FIG. 5A

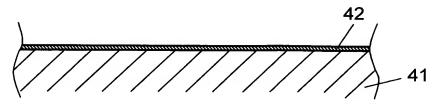


FIG. 5B

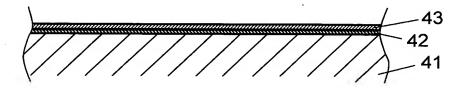
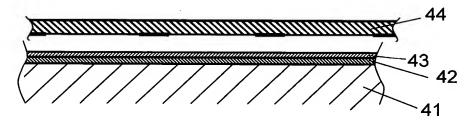
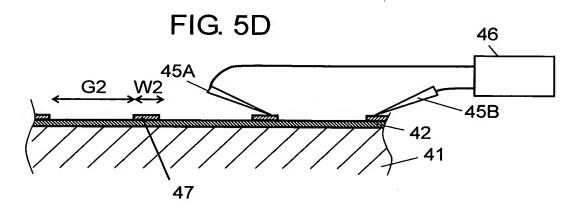


FIG. 5C





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| Reference marks in the drawings: | |
|----------------------------------|-----------------------|
| 1. | PDP |
| 2. | front substrate |
| 3. | glass substrate |
| 4. | scan electrode |
| 5. | sustain electrode |
| 4a, 5a. | transparent electrode |
| 4b, 5b. | bus electrode |
| 6. | display electrode |
| 7. | light-shield |
| 8. | dielectric layer |
| 9. | protective layer |
| 10. | rear substrate |
| 11. | glass substrate |
| 12. | address electrode |
| 13. | dielectric layer |
| 14. | rib |
| 15R, 15G, 15 | B phosphor layer |
| 16. | discharge space |
| 17. | discharge cell |
| 18a, 18b | black layer |

conductive layer

slit

19. 20.